

IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently Amended) An array substrate comprising:
 - a display area in which pixel electrodes are formed,
 - a scanning line arranged between the pixel electrodes,
 - a signal line crossing over the scanning line interposing an insulating layer therebetween,
 - a terminal to which a scanning signal is applied,
 - an extended scanning line formed from a layer of said signal line for electrically connecting the scanning line with the terminal, and
 - a connecting part for connecting said scanning line with said extended scanning line by said pixel electrodes,
 - wherein said connecting part is formed ~~in such a manner as to have a largest area neighboring said display area~~ as large as possible, as long as said connecting part is not electrically connected to an adjacent pattern, and to maintain the connecting with said scanning line adjacent to said connecting part.
2. (Previously Presented) The array substrate of claim 1 comprising:
 - an auxiliary capacitance line arranged parallel to the scanning line,
 - a collected auxiliary capacitance line arranged in parallel to the signal line and electrically connected to the auxiliary capacitance line,
 - a terminal to which a common signal is applied, and
 - an extended auxiliary capacitance line for connecting the collected auxiliary capacitance line with the terminal for the common signal, said extended auxiliary capacitance line being formed only of the same conductive film as for said signal line.
3. (Currently Amended) An array substrate comprising:
 - a display area in which pixel electrodes are formed,
 - a scanning line arranged between the pixel electrodes,
 - an auxiliary capacitance line arranged in parallel to the scanning line,

a signal line crossing over the scanning line and the auxiliary capacitance line interposing an insulating layer therebetween,

a collected auxiliary capacitance line arranged in parallel to the signal line and electrically connected to the auxiliary capacitance line,

a terminal to which a common signal is applied, and

an extended auxiliary capacitance line formed from a conductive film for connecting the collected auxiliary capacitance line with the terminal, and

a connecting part for connecting said collected auxiliary capacitance line with said extended auxiliary capacitance line by said pixel electrodes,

wherein the extended auxiliary capacitance line is formed from a layer of said signal line, and said connecting part is formed ~~in such a manner as to have a largest area neighboring said display area~~ is formed as large as possible, as long as said connecting part is not electrically connected to an adjacent pattern in order to obtain a small value contact resistance, and to prevent said common signal from delaying.

4. (Cancelled)

5. (Previously Presented) The array substrate of claim 1, wherein the extended scanning line is formed only of the same conductive film as for the pixel electrodes, instead of the same conductive film as for said signal line.

6. (Previously Presented) The array substrate of claim 1, wherein the extended scanning line is electrically connected to the scanning line at the neighborhood of the display area and electrically connected to the terminal for the scanning signal at the neighborhood of the terminal.

7. (Cancelled)

8. (Previously Presented) The array substrate of claim 2, wherein

the extended auxiliary capacitance line is formed only of the same conductive film as for the pixel electrodes, instead of the same conductive film as for the signal line.

9. (Previously Presented) The array substrate of claim 8, wherein
the extended auxiliary capacitance line is electrically connected to the collected auxiliary capacitance line at the neighborhood of the display area and electrically connected to the terminal for the common signal at the neighborhood of the terminal.
10. (Previously Presented) The array substrate of claim 2, wherein
the auxiliary capacitance line, the collected auxiliary capacitance line and the scanning line are formed from the conductive film of same layer.
11. (Previously Presented) The array substrate of claim 2, wherein
the collected auxiliary capacitance line and the extended scanning line are crossing, interposing an insulating layer therebetween.
12. (Original) The array substrate of claim 1, wherein aluminum or aluminum alloy is used for material of the scanning line.
- 13-14. (Cancelled)
15. (Original) The array substrate of claim 1, wherein
the scanning line and the extended scanning line are electrically connected via a conductive film of the same layer as that for the pixel electrode.
16. (Original) The array substrate of claim 2, wherein
the collected auxiliary capacitance line and the extended auxiliary capacitance line are electrically connected via a conductive film of the same layer as that for the pixel electrode.
17. (Original) The array substrate of claim 1, wherein

either of the scanning line or the extended scanning line is formed in a grid or ladder like shape at a region in which the scanning line and the extended scanning line are overlapped within a connecting portion between the scanning line and the extended scanning line.

18. (Original) The array substrate of claim 2, wherein

either of the collected auxiliary capacitance line or the extended auxiliary capacitance line is formed in a grid or ladder like shape at a region in which the collected auxiliary capacitance line and the extended auxiliary capacitance line are overlapped within a connecting portion between the collected auxiliary capacitance line and the extended auxiliary capacitance line.

19-21. (Cancelled)

22. (Currently Amended) An array substrate comprising:

- a display area in which pixel electrodes are formed,
- a scanning line arranged between the pixel electrodes,
- a signal line crossing over the scanning line interposing an insulating layer therebetween,
- a first terminal to which a scanning signal is applied,
- an extended scanning line formed from a layer of said signal line for electrically connecting the scanning line with the first terminal,
- a connecting part for connecting said scanning line with said extended scanning line by said pixel electrode,
- an auxiliary capacitance line arranged parallel to the scanning line,
- a collected auxiliary capacitance line arranged in parallel to the signal line and electrically connected to the auxiliary capacitance line,
- a second terminal to which a common signal is applied, and
- an extended auxiliary capacitance line for connecting the collected auxiliary capacitance line with the second terminal for the common signal, said extended auxiliary capacitance line being formed only of the same conductive film as for said signal line,

wherein the auxiliary capacitance line, the collected auxiliary capacitance line and the scanning line are formed from the conductive film of same layer, and

wherein said connecting part is formed ~~in such a manner as to have a largest area neighboring said display area~~ as large as possible, as long as said connecting part is not electrically connected to an adjacent pattern, and to maintain the connecting with said scanning line adjacent to said connecting part.